

<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary) Express Mail Number <u>EV686354931US</u>				Docket Number (Optional) <b>100-19811 (P05402-D01-F1)</b>		Application Number <b>10/772,863</b>		
<div style="position: relative; height: 100px;"> <div style="position: absolute; top: 0; left: 0; width: 100%; height: 100%; border: 1px solid black; border-radius: 50%; text-align: center; line-height: 100px; font-size: 24px; font-weight: bold;">O I P E</div> <div style="position: absolute; top: 10%; left: 10%; font-size: 12px;"> APR 19 2006  EXAMINER  INITIAL  REF  OFFICE </div> </div>				Applicant(s) <b>Abdalla Aly Naem, et al.</b>				
				Filing Date <b>February 4, 2004</b>		Group Art Unit <b>2823</b>		
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
WB		*6,103,603	08/15/00	Han	438	532	04/24/98	
WB		*6,512,252 B1	01/28/03	Takagi, et al.	257	192	11/15/00	
<b>U.S. PATENT APPLICATION PUBLICATIONS</b>								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
WB		*2002/0179946 A1	12/05/02	Hara, et al.	257	288	04/18/01	
<b>FOREIGN PATENT DOCUMENTS</b>								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES NO	
<b>OTHER DOCUMENTS</b> (Including Author, Title, Date, Pertinent Pages, Etc.)								
WB	/	Stanley Wolf, et al., "Silicon Processing For The VLSI Era", Volume 1, Processing Technology, Lattice Press, 1986, pp. 155.						
EXAMINER				DATE CONSIDERED				
/William Brewster/				18 May 2006				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								